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(74) Agent: **KIMURA, Mitsuru**; 2nd Floor, Kyohan Building, 7, Kandanishiki-cho 2-chome, Chiyoda-ku, Tokyo 101-0054 (JP).

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(71) Applicant (for all designated States except US): **TOKYO ELECTRON DEVICE LIMITED [JP/JP]**; 1, Higashikata-cho, Tsuzuki-ku, Yokohama-shi, Kanagawa 224-0045 (JP).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **MURAKAMI, Kazuaki [JP/JP]**; 1-2-307, Kasugakoen 4-chome,, Kasuga-shi, Fukuoka 816-0811 (JP). **KIKUCHI, Syuichi [JP/JP]**; c/o Tokyo Electron Device Limited,, Basyonotsuji Building, 3-16, Ichiban-cho 3-chome, Aoba-Ku, Sendai-Shi, Miyagi 980-0811, (JP).

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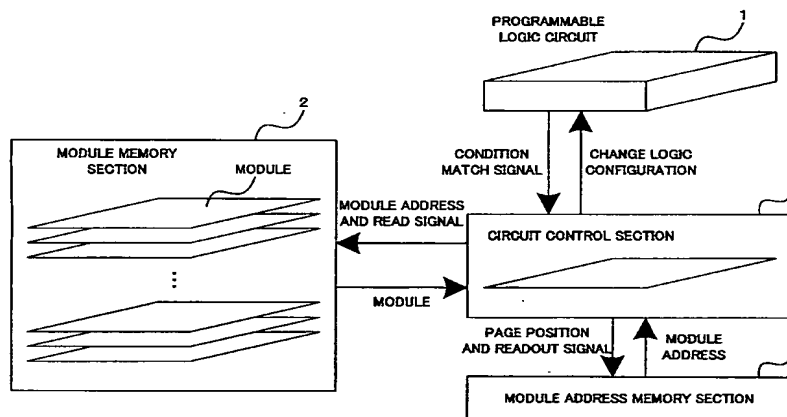
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(54) Title: PROGRAMMABLE LOGIC CIRCUIT CONTROL APPARATUS, PROGRAMMABLE LOGIC CIRCUIT CONTROL METHOD AND PROGRAM



(57) Abstract: Disclosed is a programmable logic circuit control apparatus and method, and a program, which facilitate smooth execution of multiple processes in complicated procedures. A module address memory section (3) stores data indicating the address of a module to be read by a circuit control section (4) or a condition for a branch process and a jump distance for each page. The circuit control section (4) reads data stored on the topmost page in the module address memory section (3). According to the data, the circuit control section (4) performs reading of a module, reconfiguration of a programmable logic circuit (1) and reading of data of the next page, or jumps. Every time a new page is read, the circuit control section (4) performs processes according to data stored on that page, thereby sequentially reconfiguring the programmable logic circuit (1).



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